



# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of :

Peter Korger

### Serial No. :

09/738,485

### Group Art Unit :

2183

### Filed :

December 15, 2000

### Examiner :

Huisman, David J.

### For :

Configurable Hardware Register  
Stack for CPU Architectures

### Atty Docket :

1496.00059 / 99-339

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

2/07/06

Connie del Castillo

Date

Signature

## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

### **Official Draftsman**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Respectfully submitted,

Timothy Croll

Reg. No. 36,771

Date: 6 FEB 06